

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:	Atty. Docket No.: 003921.00190
Jean BARBIER, et al.	
Serial No.: Continuation of 10/086,813	Group Art Unit: TBD
Filed: March 23, 2004	Examiner: TBD
For: RECONFIGURABLE INTEGRATED CIRCUIT WITH INTEGRATED DEBUGGING FACILITIES AND SCALABLE PROGRAMMABLE INTERCONNECT	

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examinations on the merits, please amend the above-identified application as follows:

Amendments to the Title begin on page 2 of this paper.

Amendments to the Specification begin on page 3 of this paper.

Amendments to the Claims are reflected in the Listing of Claims, which begins on page 4 of this paper.

Remarks/Arguments begin on page 5 of this paper.

Amendments to the Title:

Please change the title to read:

“A RECONFIGURABLE INTEGRATED CIRCUIT WITH INTEGRATED
DEBUGGING FACILITIES AND SCALABLE PROGRAMMABLE
INTERCONNECT”